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Drwg Chng  
SDavis  
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Applicant: Bulucea, Constantin  
Assignee: National Semiconductor Corporation  
Title: Gate-Enhanced Junction Varactor  
Serial No.: 09/903,059 Filing Date: July 10, 2001  
Examiner: D. Farhani Group Art Unit: 2814  
Docket No.: NS-4971 US

San Jose, California  
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COMMISSIONER FOR PATENTS  
Washington, D. C. 20231

AMENDMENT TO DRAWINGS

Sir:

Responsive to the Office Action mailed 14 March 2002, the drawings of the above patent application should be amended as described below.

In Fig. 19a, reference symbol "262" and the associated lead line should be deleted, and reference symbol "264" should be changed to "262".

In Fig. 20a, reference symbol "260" should be changed to "266".

In Fig. 20b, reference symbols "262", "264", "266", and "268" should be respectively changed to "268A", "268B", "268C", and "268D".

In Fig. 27, reference symbol "296" should be changed to "294".

Enclosed are copies of the relevant drawings sheets in which the foregoing changes are indicated in red.

LAW OFFICES OF  
SKJERVEN MORRILL LLP  
25 METRO DRIVE  
SUITE 700  
SAN JOSE, CA 95110  
(408) 453-9200  
FAX (408) 453-7979